



**LESSON PLAN,SESSION-SUMMER-2024**  
**SWAMI VIVEKANANDA SCHOOL OF ENGG & TECH, BBSR**

DISCIPLINE- ETC ENGG.	SEMISTER- 4TH	NEME OF THE FACULTY-ER. SRIDHARA KUMAR RATH
SUBJECT- MP & MC	NO OF CLASS ALLOTTED/WEEK-5	SEMESTER FROM- 16.01.2024 TO 26.04.2024
WEEK	DATE	TOPICS
3RD	16.01.2024	Microprocessor (Architecture and Programming-8085-8-bit)
	17.01.2024	Introduction to Microprocessor and Microcomputer & distinguish between them.
	19.01.2024	Concept of Address bus, Data bus, Control bus & System Bus
	20.01.2024	General Bus structure Block diagram.
4TH	22.01.2024	Basic Architecture of 8085 (8 bit) Microprocessor
	23.01.2024	Signal Description (Pin diagram) of 8085 Microprocessor
	24.01.2024	Register Organizations, Distinguish between SPR & GPR, Timing & Control Module
	27.01.2024	Stack, Stack pointer & Stack top
5TH	29.01.2024	Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM)
	30.01.2024	Instruction Set and Assembly Language Programming
	31.01.2024	with examples.
1ST	02.02.2024	Addressing modes in instructions with suitable examples.
	03.02.2024	Machine Control)
2ND	05.02.2024	Simple Assembly Language Programming of 8085
	06.02.2024	Simple Addition & Subtraction
	07.02.2024	Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits
	09.02.2024	Counters & Time delay (Single Register, Register Pair, More than Two Register)
	10.02.2024	Looping, Counting & Indexing (Call/JMP etc)
3RD	12.02.2024	Stack & Subroutine programes
	13.02.2024	Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer
	16.02.2024	Compare between two numbers
	17.02.2024	Array Handling (Largest number & smallest number in the array)
4TH	19.02.2024	Memory & I/O Addressing
	20.02.2024	TIMING DIAGRAMS
	21.02.2024	the concept of timing diagram
	23.02.2024	Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle
	24.02.2024	instruction)
	26.02.2024	Microprocessor Based System Development Aids
5TH	27.02.2024	Concept of interfacing
	28.02.2024	Define Mapping & Data transfer mechanisms - Memory mapping & I/O Mapping
1ST	01.03.2024	Concept of Memory Interfacing:- Interfacing EPROM & RAM Memories
	02.03.2024	Concept of Address decoding for I/O devices
	04.03.2024	Programmable Peripheral Interface: 8255
2ND	05.03.2024	ADC & DAC with Interfacing
	06.03.2024	Interfacing Seven Segment Displays

3RD	09.03.2024	Generate square waves on all lines of 8255
	11.03.2024	Design Interface a traffic light control system using 8255.
	12.03.2024	Design interface for stepper motor control using 8255.
	13.03.2024	Basic concept of other Interfacing DMA controller, USART
	15.03.2024	Microprocessor (Architecture and Programming-8086-16 bit)
	16.03.2024	Register Organisation of 8086
4TH	18.03.2024	Internal architecture of 8086
	19.03.2024	Signal Description of 8086, General Bus Operation & Physical Memory Organisation.
	20.03.2024	Revision
	22.03.2024	Class Test
	23.03.2024	Maximum Mode & Timings
5TH	25.03.2024	Maskable Interrupt
	27.03.2024	Directives and Operators
	29.03.2024	Simple Assembly language programming using 8086 instructions
	30.03.2024	Microcontroller (Architecture and Programming-8 bit)
1ST	02.04.2024	Distinguish between Microprocessor & Microcontroller
	03.04.2024	8 bit & 16 bit microcontroller
	05.04.2024	CISC & RISC processor
	06.04.2024	Architecture of 8051 Microcontroller
2ND	08.04.2024	Signal Description of 8051 Microcontrollers
	09.04.2024	Memory Organisation-RAM structure, SFR
	10.04.2024	Registers, timers, interrupts of 8051 Microcontrollers
	12.04.2024	Addressing Modes of 8051
	13.04.2024	LOOP, CALL Instructions, I/O Port Programming
3RD	15.04.2024	LOOP, CALL Instructions, I/O Port Programming
	16.04.2024	Interrupts, Timer & Counters
	19.04.2024	Interrupts, Timer & Counters
	20.04.2024	Serial Communication
4TH	22.04.2024	Microcontroller Interrupts and Interfacing to 8255
	23.04.2024	Microcontroller Interrupts and Interfacing to 8255
	24.04.2024	REVISION
	26.04.2024	REVISION

  
H. O. D.

  
DEAN (ACADEMICS)

  
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